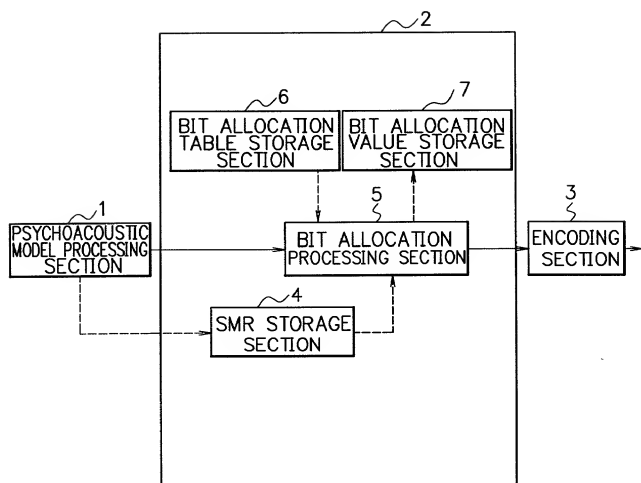


F I G. 1



## FIG. 2

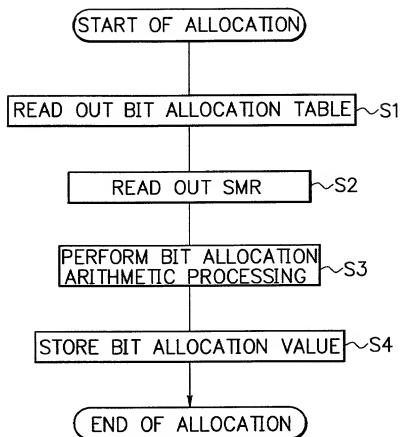


FIG. 3

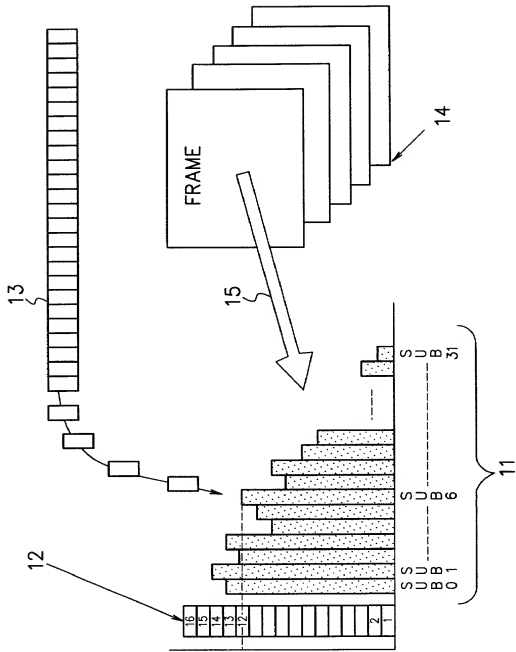
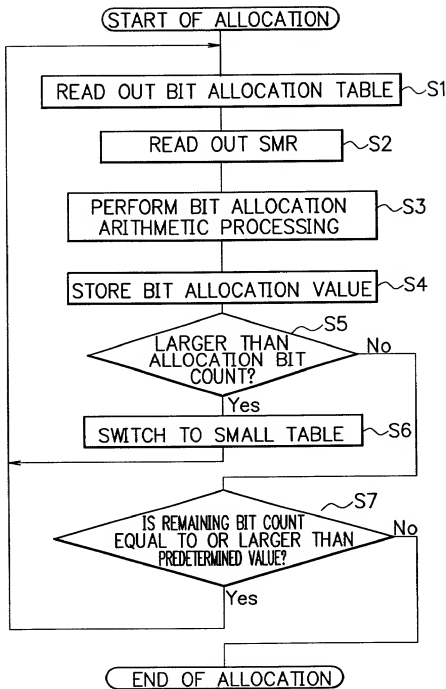
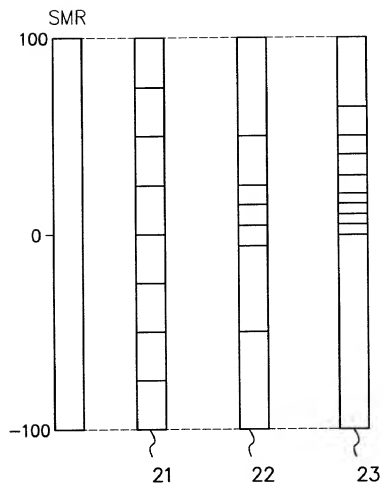


FIG. 4



# FIG. 5





[illegible][illegible][illegible][illegible]

# F I G. 11

BIT ALLOCATION COUNT j	CORRESPONDING SMR LEVEL
16	74.00
15	68.00
14	62.00
13	56.00
12	50.00
11	44.00
10	38.00
9	32.00
8	26.00
7	21.00
6	16.00
5	11.00
4	7.00
3	0.00
2	-20.00
1	-50.00
0	-

# F I G. 12

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
7	6	5	4	3	3	3	3	3	3	3	3	2	2	2	2
16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
2	2	2	2	2	2	1	1	1	1	1	1	0	0	0	0



# FIG. 13

## PRIOR ART

